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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/853;288 05/11/2001		Richard H. Sherman	00-1016	5854
7590 11/03/2004			EXAMINER	
Keith D. Nelson			NORRIS, TREMAYNE M	
Lockheed Martin Corporation Building 220, Mail Stop A08		ARTUNIT	PAPER NUMBER	
P.O. Box 49041			2137	
San Jose, CA	95161-9041		DATE MAILED: 11/03/2004	,

Please find below and/or attached an Office communication concerning this application or proceeding.

						
Office Action Summary		Application No.	Applicant(s)			
		09/853,288	SHERMAN ET AL.			
		Examiner	Art Unit			
TI. MAII NO DATE - 641:	• • • • • • • • • • • • • • • • • • • •	Tremayne M. Norris	2137			
Period for Reply	ommunication app	ears on the cover sneet with the	he correspondence address			
A SHORTENED STATUTORY PE THE MAILING DATE OF THIS CC - Extensions of time may be available under the after SIX (6) MONTHS from the mailing date o- If the period for reply specified above is less the If NO period for reply is specified above, the mailing the period for reply is specified above, the mailing to reply within the set or extended perion and reply received by the Office later than three earned patent term adjustment. See 37 CFR	MMUNICATION. provisions of 37 CFR 1.13 f this communication. nan thirty (30) days, a reply naximum statutory period w od for reply will, by statute, see months after the mailing	66(a). In no event, however, may a reply livithin the statutory minimum of thirty (30 fill apply and will expire SIX (6) MONTHS cause the application to become ABAND	be timely filed) days will be considered timely. from the mailing date of this communication. ONED (35 U.S.C. § 133).			
Status						
1) Responsive to communication	on(s) filed on 11 M	av 2001.				
2a) This action is FINAL .						
<u> </u>	· 					
closed in accordance with th	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) Claim(s) 1-8 is/are pending in 4a) Of the above claim(s) is/are allower 6) Claim(s) 1-8 is/are rejected. 7) Claim(s) 1-8 is/are rejected. 7) Claim(s) is/are object is/are subject is	is/are withdraved.					
Application Papers						
· · · · · · · · · · · · · · · · · · ·	ay 2001 is/are: a)[any objection to the concluding the correction	☑ accepted or b)☐ objected drawing(s) be held in abeyance. on is required if the drawing(s) is	See 37 CFR 1.85(a). s objected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119						
<u> </u>	ne of: priority documents priority documents copies of the prior ternational Bureau	s have been received. s have been received in Appli ity documents have been rec (PCT Rule 17.2(a)).	cation No eived in this National Stage			
Attachment(s)						
1) Notice of References Cited (PTO-892)		4) Interview Summ				
Notice of Draftsperson's Patent Drawing Information Disclosure Statement(s) (PTO Paper No(s)/Mail Date		Paper No(s)/Ma 5) Notice of Inform 6) Other:	all Date nal Patent Application (PTO-152)			

DETAILED ACTION

Claim Objections

1. Claim 7 is objected to because of the following informalities: There is an unnecessary parenthesis in the claim. Appropriate correction is required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1,6-8 are rejected under 35 U.S.C. 102(b) as being anticipated by Pecora et al (US pat 5,379,346).

Regarding claim 1, Pecora teaches a chaos privacy system for use in communicating an analog signal, the system comprising:

a transmitter comprising:

a key stream generator comprising a chaotic circuit that generates a key stream in response to a private key parameter, and transmits a key synchronization parameter (col.6 lines 8-19; col.9 lines 60-67; col.10 lines 29-33); and

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a transmitting chaotic circuit that processes the analog information signal and the key stream to generate and transmit a cipherwave (col.6 lines 8-19; col.9 lines 60-67; col.10 lines 29-33); and

a receiver, for receiving the transmitted cipherwave, the transmitted key synchronization parameter, and a copy of the private key parameter (col.9 line 60 thru col.10 line 33), that comprises:

a key stream generator comprising a chaotic circuit that processes the copy of the private key parameter and the transmitted key synchronization parameter to generate a copy of the key stream; and

a receiving chaotic circuit that processes the copy of the key stream and the cipherwave to demodulate the cipherwave to recover and output the information signal (col.9 line 60 thru col.10 line 33).

Regarding claim 6, Pecora teaches a chaos privacy method for use in communicating an analog information signal, the method comprising the steps of:

Generating a random key stream using a chaotic circuit;

Processing analog information signals and the random key stream to generate a cipherwave that has a uniform probability density for all information signals and the random key streams;

Transmitting the cipherwave and a public key over a communication channel (col.10 lines 31-33);

Receiving the cipherwave, and public key;

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Synchronizing to the public key using a chaotic circuit to produce a copy of the random key stream;

And processing the cipherwave and the copy of the random key stream to reconstruct the analog information signal (col.2 lines 18-47; col.9 line 61 thru col.10 line 33).

Regarding claim 7, Pecora teaches the cipherwave is generated by multiplying the information signals by the random key stream (col.9 line 67 thru col.10 line 6).

Regarding claim 8, Pecora teaches the information signal is generated by multiplying the cipherwave by the random key stream (col.2 lines 40-47; col.9 line 67 thru col.10 line 33).

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 2-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Pecora, and further in view of Bernstein et al (US pat 5,007,087).

Regarding claim 2, Pecora teaches the system of claim 1. What Bernstein teaches that Pecora does not teach is the key stream generator in the transmitter comprises a first chaotic circuit comprising a first sample and hold circuit coupled to a first voltage controlled oscillator; and

a second chaotic circuit comprising a second sample and hold circuit coupled to a second voltage controlled oscillator;

wherein an output of the first voltage controlled oscillator provides an input to the second sample and hold circuit, a first output of the second voltage controlled oscillator provides an input to the first sample and hold circuit, the first voltage controlled oscillator outputs the key stream, and a second output of the second voltage controlled oscillator outputs the key synchronization parameter (fig.9; col.9 lines19-37). It would have been obvious to one of ordinary skill in the art at the time of the invention to combine Pecora's synchronized chaotic system with Bernstein's apparatus for generating secure random numbers using chaos in order to provide a secure system with randomness properties in a chaotic environment (Bernstein col.1 lines 22-31; col.2 lines 24-26; col.3 lines 16-29).

Regarding claim 3, Pecora and Bernstein in combination teach the method of claim 2, in addition Pecora teaches the transmitting chaotic circuit comprises a hard limiting circuit for receiving the key stream from the key stream generator and for

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converting the output of the first voltage controlled oscillator to selected fixed values; and

an analog multiplier circuit for multiplying the information signal with the sampled random signal to produce the cipherware (col.9 line 67 thru col.10 line 6).

Regarding claim 4, Pecora teaches a key stream generator in the receiver (col.2 lines 18-21; col.2 lines 40-47). What Bernstein teaches that Pecora does not teach is a third chaotic circuit comprising a third sample and hold circuit coupled to a third voltage controlled oscillator; and

a fourth chaotic circuit comprising a fourth sample and hold circuit coupled to a fourth voltage controlled oscillator;

wherein the key synchronization parameter is input to the third sample and hold circuit, an output of the third voltage controlled oscillator provides an input to the fourth sample and hold circuit, an output of the fourth voltage controlled oscillator provides an input to the third sample and hold circuit, and the fourth voltage controlled oscillator outputs the copy of the key stream (fig.9; col.9 lines19-37). It would have been obvious to one of ordinary skill in the art at the time of the invention to combine Pecora's synchronized chaotic system with Bernstein's apparatus for generating secure random numbers using chaos in order to provide a secure system with randomness properties in a chaotic environment (Bernstein col.1 lines 22-31; col.2 lines 24-26; col.3 lines 16-29).

Regarding claim 5, Pecora and Bernstein in combination teach the method of claim 4, in addition Pecora teaches the receiving chaotic circuit comprises a hard limiting circuit for receiving the copy of the key stream from the third voltage controlled oscillator and for converting it to selected fixed values;

a sample and hold circuit for sampling the hard limited output of the hard limiting circuit at a fixed frequency to produce a sampled random signal; and

an analog multiplier circuit for multiplying the cipherware with the sampled random signal to recover the information signal (col.9 line 61 thru col.10 line 33; col.19 line 35 thru col.20 line 15).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tremayne M. Norris whose telephone number is (571) 272-3874. The examiner can normally be reached on M-F 7:30AM-5:00PM alternate Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Andrew Caldwell can be reached on (571) 272-3868. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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andrew Caldwell Andrew Caldwell

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Tremayne Norris

October 28, 2004

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